

NOVELIC is a high-tech company developing products in the fields of sensors, industrial electronics and wireless communications. We cover all subsystems of a modern electronic product under one roof: Embedded HW/FW, FPGA, ASIC, RF IC, etc. NOVELIC has dynamic and creative working environment with excellent working conditions. Responsibility and opportunity to lead and drive the projects is given. Stable long-term career roadmap for proven team members is our goal, so as yours.

We invest in excellence, offer excellence and ask for excellence.

We are pleased to enable students of the final years to be a part of our internship program in the area of:

FPGA Design

As an intern within Novelic internship program, you will have the opportunity to work on various FPGA and SoC design projects. You will be responsible for designing and implementing various IPs on FPGA fabric in collaboration with our experienced engineers as a part of our efficient team.

What competences we are looking for:

- Final year/master studies student at relevant University in area of Electronics
- Strong theoretical background in analog and digital electronics
- VHDL/Verilog, Matlab/Simulink and Python are an advantage
- Basic knowledge of FPGA architecture (Xilinx, Altera)
- Solid knowledge of English, both written and spoken

As a part of our team you should be:

- Interest in solving complex and time critical problems
- Positive, motivated and eager to learn new things
- Able to think creatively and produce "outside of the box" solutions
- Prepared to tackle new technologies

Remainder:

Internship program is with scholarship provided

If completion of the internship were to be successful, results can be used for writing of B.Sc. or M.Sc. thesis, subject to approval by the supervising professor at the University and Novelic. Moreover, satisfying results of the full-time internship will increase the chance of full-time position within the Novelic Design Centre in Belgrade to the graduated intern.

If you are interested in internship in a dynamic environment with possibility to improve knowledge in the area of FPGA and to work with some of the best engineers, send us your CV and a brief motivation letter in PDF format to the following e-mail: internship@novelic.com with following subject: <NIC_Internship_FPGA_YourName>

For more insight visit our website: www.novelic.com

We look forward to meeting you!

NOVELIC team