

NOVELIC is a high-tech company developing products in the fields of sensors, industrial electronics and wireless communications. We cover all subsystems of a modern electronic product under one roof: Embedded HW/FW, FPGA, ASIC, RF IC, etc. **NOVELIC** has dynamic and creative working environment with excellent working conditions. Responsibility and opportunity to lead and drive the projects is given. Stable long-term career roadmap for proven team members is our goal, so as yours.

We invest in excellence, offer excellence and ask for excellence.

We are pleased to offer a full-time position for:

ASIC Design Engineer

You should have:

- Education level: Bachelor or Masters' Degree
- Experience with VHDL, Verilog or System Verilog Languages
- Excellent testing and troubleshooting skills

You should be experienced in the following:

- RTL level modelling and implementation in VHDL and/or Verilog
- C/C++. System C is an advantage
- FPGA/ASIC/DSP implementation
- Matlab and Simulink. Signal processing
- Knowledge of embedded HW/SW is considered advantageous

This position includes responsibilities for development, integration and test of digital subsystem features and interfaces in ASIC hardware.

We offer working in a dynamic environment in a team with good engineers, including attractive salary packet with private pension and health insurance, bonus, highly paid business trips etc.

If you are interested in becoming a part of Novelic, please send us your CV and a brief motivation letter (in PDF format) to the following e-mail address: careers@novelic.com with the subject <NIC_ASIC_201810_YourName>

Feel encouraged to apply promptly, the first matching candidate will be taken.

We look forward to meeting you!
Novelic team