

NOVELIC is a high-tech company developing products in the fields of sensors, industrial electronics and wireless communications. We cover all subsystems of a modern electronic product under one roof: Embedded HW/FW, FPGA, ASIC, RF IC, etc. **NOVELIC** has dynamic and creative working environment with excellent working conditions. Responsibility and opportunity to lead and drive the projects is given. Stable long-term career roadmap for proven team members is our goal, so as yours.

We invest in excellence, offer excellence and ask for excellence.

We are pleased to offer a full-time position for:

Digital ASIC and FPGA Engineer

You should have:

- Education level: Bachelor or Masters' Degree
- Experience in high-scale projects with reporting, presentations, conferences, etc.
- Proficiency in English, both written and spoken

You should be experienced in the following:

- RTL level modelling and implementation in VHDL and/or Verilog.
- SystemC is an advantage
- FPGA/ASIC/DSP implementation
- Matlab and Simulink
- Signal processing theory

Critical aspects of the role in general include architecture and implementation of building blocks inside of complex digital IC systems. We encourage ideas, ability to understand algorithms and blocks to the deepest level and focus on implementation. We encourage people ready to think wide and deep on the architecture level. At least one finished SoC project implemented on FPGA or ASIC platform is deemed as necessary.

We offer working in a dynamic environment in a team with good engineers, including attractive salary packet with private pension and health insurance, bonus, highly paid business trips etc.

If you are interested in becoming a part of Novelic, please send us your CV and a brief motivation letter (in PDF format) to the following e-mail address: careers@novelic.com with the subject <NIC_DAF_201807_YourName>

*We look forward to meeting you!
Novelic team*